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Docket No.: GR 97 P 6457 P

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C. Brown

By: Markus Noll

Date: May 7, 2001 800 MAIL ROOM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor : Jörg Berthold et al.
Applic. No. : 09/595,860 ✓
Filed : June 16, 2000
Title : Integrated Electrical Circuit and Method for Fabricating It
Examiner : Cuong Q. Nguyen - Art Unit: 2811 ✓

R E S P O N S E

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

Sir:

Responsive to the Office Action dated April 5, 2001, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, claims 1-23, for prosecution at this time.

Applicants reserve the right to file a divisional application including the method claim of group II at a later date.

In view of the foregoing, the early issuance of an Action on the merits of claims 1-23 is solicited.

Respectfully submitted,

Markus Noll
For Applicants

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Date: May 7, 2001

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